

A COMPACT FRACTAL DIPOLE ANTENNA FOR 915 MHz AND 2.4 GHz RFID TAG APPLICATIONS

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Abstract—A compact printed dipole antenna using fractal shape for Radio Frequency Identification (RFID) is presented. The proposed antenna consists of a third iteration fractal tree structure with the aim of reducing the antenna size. It occupies a volume of $78 \times 30 \times 1.58 \text{ mm}^3$ and the radiator is composed of two arms. The antenna has been designed and optimized to cover the bi-band for passive RFID tag at 915 MHz and 2.4 GHz. A parametric study of the proposed antenna was carried out in order to optimize the main parameters. Details of the proposed antenna design and measurement results are presented and discussed.

1. INTRODUCTION

Radio Frequency Identification (RFID) is an automatic identification method on storing and remotely retrieving data using devices called RFID tags or transponders. An RFID tag is a small object that can be attached to or incorporated into a product, animal or person. RFID tag contains an application-specific integrated circuit (ASIC) chip and an antenna to enable it to receive and respond to Radio-Frequency (RF) queries from RFID reader or interrogator. Passive tags require no internal power source, whereas active tags require a power source. Today most implantations involve passive technology for its low cost [1].

Several frequency bands have been standardized for this technology. Low-Frequency (LF, 125–134 KHz) and High-frequency (HF, 13.56 MHz) systems are the most mature and worldwide diffused technologies. Tags at these frequencies use inductive coupling between two coils in order to supply energy to the tag and send information.

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Ultra-High Frequency (UHF, 860–960 MHz) and microwave frequency (2.4 GHz and 5.8 GHz) systems use the technique of far field back-scatter modulation.

Low-frequency (LF) and high-frequency (HF) active RFIDs operating with battery power and a moderate sized antenna can transmit over long distances. Thus, they can be used for livestock tracking, access control, point of sale, etc. Nonetheless, the biggest potential lies in ultra high-frequency (UHF) and microwave frequency. Passive RFIDs which operate without battery and a very small-size antenna can be used for item tracking, especially useful for global supply chain management. The challenge for this technology is to increase the read range and its flexibility to environment factors for different applications.

Several papers on passive RFID tags antennas design for dual band applications have appeared in the literature in recent years, the majority of these tags combining the HF and UHF frequency [2, 3] or UHF and microwave frequency [4–6]. For the characteristics of miniaturization, how to increase the efficiency of RFID tag antenna in the limited space becomes a crucial project in this technology and the fractal theory is a solution for this issue. Fractal theory was proposed by Mandelbrot in 1975 [7]. Fractal geometries have self-similarity and space-filling nature when applied to antenna design and can realize multi-frequency and size-reducing features [8]. Several fractal geometries have been explored for antennas with special characteristics such as Sierpinski monopole [9] Koch curves monopole [10] and tree monopole [11, 12]. They verified that fractal antenna has size-reducing feature within the limited space.

In this paper, a new small-size fractal shape tag antenna is proposed for RFID communications combining the UHF and microwaves bands. The antennas dimensions have been optimized in order to obtain operation in the US RFID UHF (902–928 MHz). Nevertheless, the tag can be tuned to operate in other UHF bands corresponding to the standards used in other countries (European: 865–868 MHz; Japan: 950–956 MHz).

2. ANTENNA CONFIGURATION

The configuration of the proposed antenna is shown in Figure 1. The dipole antenna mainly consists of two radiating arms with third iteration tree fractal shape and using an inductively coupled feeding rectangular loop. Each branch allows one of its ends to branch off in two directions with a branching angle of 60° , a branch length half of the previous and a trace width of 1 mm. In the next stage of iteration,

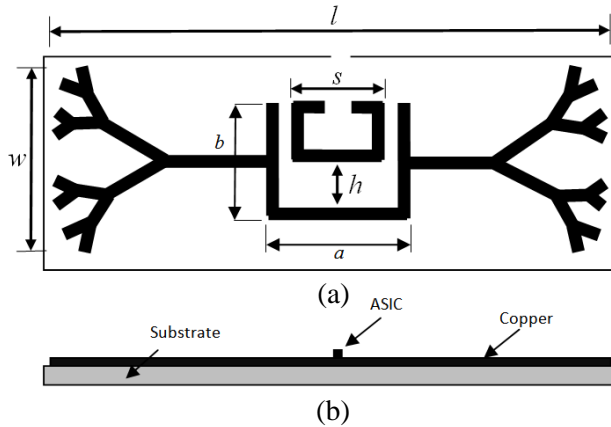


Figure 1. Geometry of the proposed antenna (a) Top view. (b) Cross-sectional view.

each of these branches is allowed to branch off again, by the same manner, and the process is continued until the third iteration. The dimensions of fractal tree branches and branching angle are taken so that the dipole without the rectangular loop is matched to the classical $50\ \Omega$ reference impedance port at 915 MHz. Therefore, the optimum length of the first branch is found to be 24 mm. The proposed antenna is printed on a low cost FR4 substrate with relative permittivity of 4.32 and thickness of 1.58 mm. The antenna dimensions are 78 mm \times 30 mm ($l \times w$). It amounts to $0.23\lambda_0 \times 0.019\lambda_0$, where λ_0 is the wavelength in free space at the centre frequency of 915 MHz. The length of the conventional dipole antenna is about half the resonant wavelength and is 163.9 mm working at 915 MHz. The size of the proposed antenna without loop reduces about 37.2% comparing with conventional dipole antenna. Consequently, the structure of fractal shape is valid as a method of reducing the size of dipole.

The two ports at the center of the loop represent the location of the chip, and the feed is combined with the antenna body with mutual coupling. By suitably adjusting the loop length “ s ” and loop-radiating body spacing “ h ”, the input impedance of the tag antenna can be complex conjugate to any desired microchip.

3. PARAMETRIC STUDY

The impedance matching between the antenna and the chip plays a crucial role in passive RFID tag design. The matching condition

directly affects the maximum distance at which a RFID reader can detect the backscattered signal from tag. To ensure the inductive complex conjugate matching, an inductively coupled loop is used. The length “ s ” and spacing “ h ” of the added structure are the key geometric parameters to control the matching. A parametric study of the proposed antenna on these two parameters of the patch radiator has been optimized. Simulations are performed using commercially available software package such as Ansoft Designer.

3.1. The Effect of the Loop Length “ s ”

The loop length “ s ” is varied to obtain optimum reactance and resistance matching. As shown in Figure 2, the real and imaginary parts as a function of frequency were simulated for different lengths of “ s ” ($s = 10, 12,$ and 12.5 mm). The other design parameters are $l = 78$ mm, $w = 30$ mm, $a = 17$ mm, $b = 13$ mm, $h = 1$ mm and a trace width of 1 mm. It is shown that the impedance of the antenna varies remarkably with the variation of the loop length s . For the proposed antenna, it can be noted that the loop length parameter influences the reactance of the input impedance of the tag antenna more strongly than the resistance, for both bands.

3.2. The Effect of the Loop-radiating Spacing “ h ”

The h parameter is the distance between the feeding loop and the antenna radiating body. As shown in Figure 3, the real and imaginary parts as a function of frequency were simulated for different spacing lengths of “ h ” ($h = 0.1, 1,$ and 3 mm). The other design parameters are $l = 78$ mm, $w = 30$ mm, $a = 17$ mm, $b = 13$ mm and $s =$

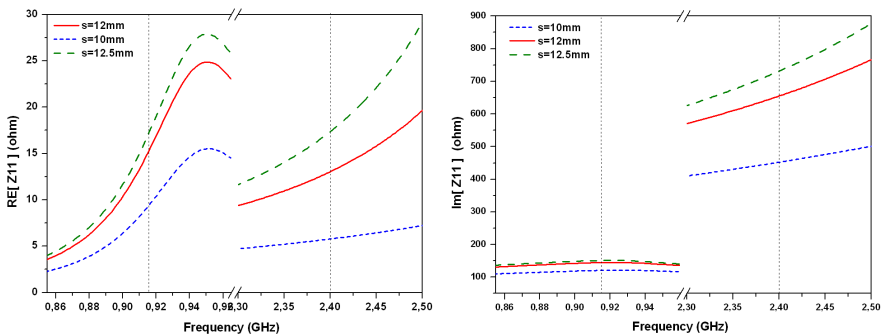


Figure 2. The effect of loop length “ s ”.

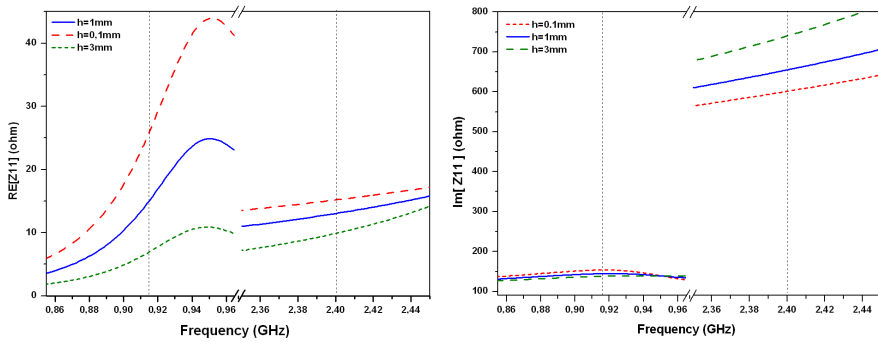


Figure 3. The effect of loop-radiating spacing “*h*”.

12 mm. It is noticed that at 915 MHz, when the loop-radiating body spacing increases, both the resistance and reactance decrease but the resistance is more affected. While at 2.4 GHz, the resistance still nearly unchanged, and the reactance increases strongly.

For the proposed tag antenna, the combination of these geometric parameters allows the conjugate impedance matching of the tag antenna to a large variety of microchips which present a conjugate input impedance ($7 < R_{chip} < 26 \Omega$) and ($110 < -X_{chip} < 151 \Omega$) at 915 MHz; ($5 < R_{chip} < 17 \Omega$) and ($450 < -X_{chip} < 740 \Omega$) at 2.4 GHz.

4. EXPERIMENTAL RESULTS

The prototype of the proposed RFID dipole antenna with the optimal geometrical parameters was fabricated. Fractal antenna is optimized to be matched with a commercial tag (EPC GEN 2), which has input impedance of $(14 - j144) \Omega$ at 915 MHz and $(14 - j650)$ at 2.4 GHz. The measurement of the return loss and input impedance of the antenna were performed using an Agilent 8719ES Vector Network Analyzer (VNA). Since the geometry of the antenna is symmetrical and the simulations are performed using symmetrical boundary, the measurement setup is able to use the mirror method [13]. Half of the balanced dipole antenna on ground plane is one half of the balanced bow tie antenna shown. This unbalanced version of the bow tie antenna is soldered on a SMA connector, which is mounted on a ground plane. It should be noted that the VNA has been pre-calibrated without input SMA connector of the test structure, so we must shift the reference plane of the *S* matrix to the ground plane in order to eliminate the additional series impedance of the SMA connectors.

The Kurokawa method for calculating the power reflection

coefficient is adapted to deal with the complex impedance of the tag antenna and the chip. This method describes a concept of power waves traveling back and forth between tag antenna and the chip by first introducing the following definitions for defining the return loss [14]:

$$\text{Return Loss} = S = 20 \log \left(\left| \frac{Z_{ant} - Z_c^*}{Z_{ant} + Z_c} \right| \right) \quad (1)$$

Z_{ant} is related to the antenna impedance and Z_c is the chip impedance. In essence, this parameter shows what fraction of maximum power available from the chip is not delivered to the antenna. The maximum readable range “ r ” can then be calculated using the Friis free-space formula as [14]:

$$r = \frac{\lambda}{4\pi} \sqrt{\frac{P_t G_t G_r (1 - S)}{P_{th}}} \quad (2)$$

where λ is the wavelength, P_t is the power transmitted by the RFID reader, G_t is the gain of the transmitting antenna, $(P_t G_t)$ is the equivalent isotropic radiated power, G_r is the gain of the receiving tag antenna, and P_{th} is the minimum threshold power necessary to power up the chip.

4.1. Impedance Matching

Figure 4 shows the fabricated antenna, to ensure conjugate matching to the considered chip, the tag antenna optimization gives $l = 78$ mm, $w = 30$ mm, $s = 12$ mm, $a = 17$ mm, $b = 13$ mm and $h = 1$ mm.

The examined antenna performances are shown below.

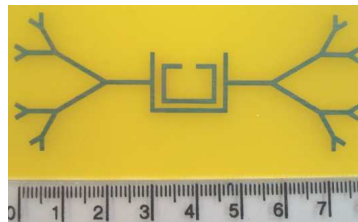


Figure 4. Photograph of the realized tag antenna.

The antenna’s measured and simulated input impedances are shown in Figure 5. The measurements are performed using the Agilent HP8719ES vector network analyser and the mirror method. It is noticed, that the input impedance is roughly $(16+j142)\Omega$ at the frequency 915 MHz, and $(18+j645)\Omega$ at the frequency 2.4 GHz

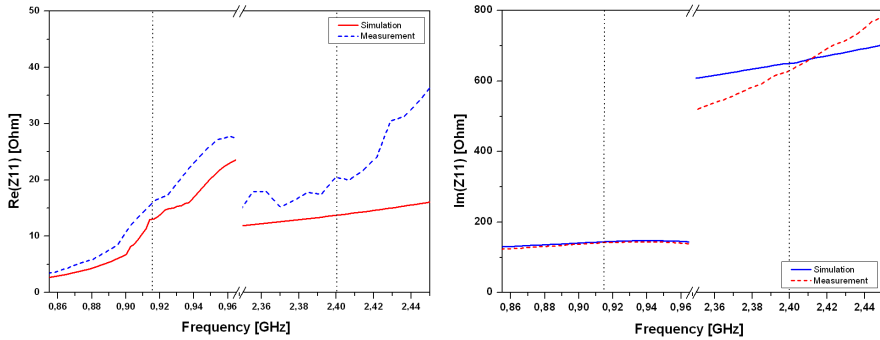


Figure 5. Measured and simulated input impedances of the antenna.

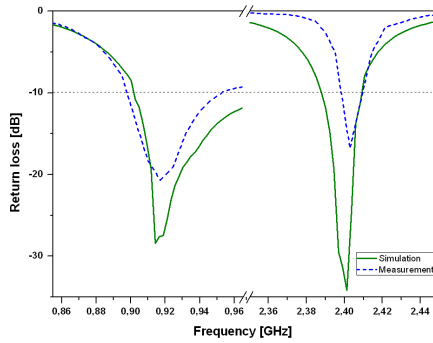


Figure 6. Measured and simulated power reflexion coefficients.

which allows a good match to the chip. As shown in the figure, the agreement between the simulated and measured reflexion coefficients is fairly good, and the discrepancy can be attributed to fabrication tolerance and calibration error.

4.2. Power Reflection Coefficient

The measured and simulated power reflection coefficients are shown in Figure 6. From the figure, we observe that the power reflection coefficient is roughly -20 dB @ 915 MHz, with an operating bandwidth of 50 MHz on a -10 dB level, and -17 dB @ 2.4 GHz with a bandwidth of 14 MHz.

4.3. Radiation Pattern

The simulated antenna’s radiation patterns (E&H plan), at 915 MHz and 2.4 GHz, are shown in Figure 7. The radiation patterns are omnidirectional on the first band and nearly and also at the high band. The simulated peak gain is about 1.11 dBi @ 915 MHz and 1.15 dBi @ 2.4 GHz.

4.4. Read Range

A comparison between theoretically and experimentally estimated read ranges is illustrated in Figure 8. Tag range was computed using formula (2), simulated and measured values for the power reflection

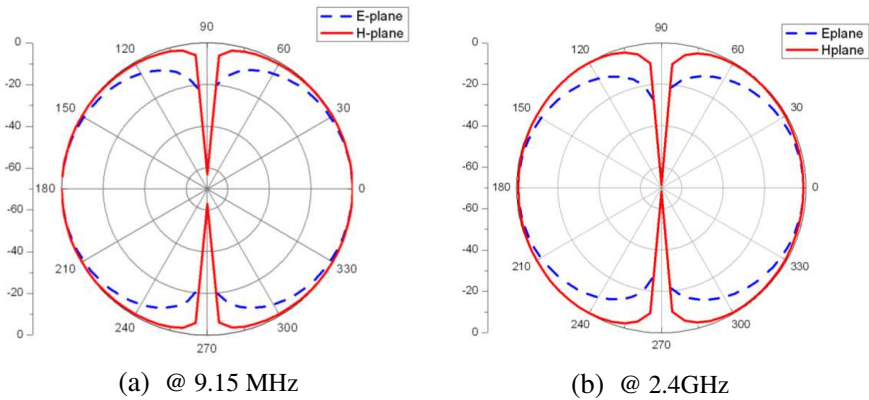


Figure 7. Radiation pattern of the antenna.

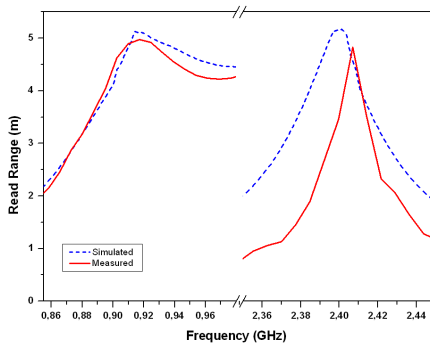


Figure 8. Estimated tag read range.

coefficient and tag antenna gain, considering the standardized EIRP = 4 W for the reader and the threshold power of the chip which is $P_{th} = -10$ dBm.

From the result, it can be seen that theoretical and experimental curves are in close agreement, and the tag presents a reading distance of about 5 m @ 915 MHz band, and 4.5 @ 2.4 GHz.

5. CONCLUSION

A new dual band RFID dipole antenna has been proposed and experimentally demonstrated. Dual band and size reduction are achieved by using third iteration tree fractal shape. The Kurokawa's method and Friis free-space formula have been applied to calculate the theoretical power reflexion coefficient and the maximum readable range, respectively. The simulated and measured results demonstrate good agreement. This type of dual band antenna can be used in different kinds of small RFID readers, wireless sensor network nodes and other compact wireless systems.

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